- Package Options Include Plastic "Small Outline" Packages, Ceramic Chip Carriers and Flat Packages, and Plastic and Ceramic DIPs
- Dependable Texas Instruments Quality and Reliability

#### description

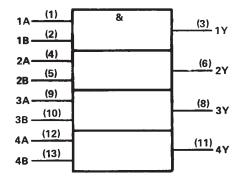
These devices contain four independent 2-input AND gates.

The SN5408, SN54LS08, and SN54S08 are characterized for operation over the full military temperature range of  $-55\,^{\circ}\text{C}$  to  $125\,^{\circ}\text{C}$ . The SN7408, SN74LS08 and SN74S08 are characterized for operation from 0° to 70°C.

FUNCTION TABLE (each gate)

INP	UTS	OUTPUT
Α	В	Υ
Н	Н	Н
L	X	L
×	L	Ļ

## logic symbol†



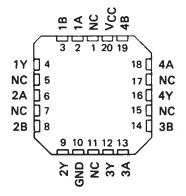
<sup>&</sup>lt;sup>†</sup>This symbol is in accordance with ANSI/IEEE Std 91-1984 and IEC Publication 617-12.

Pin numbers shown are for D, J, N, and W packages.

SN5408, SN54LS08, SN54S08 . . . J OR W PACKAGE SN7408 . . . J OR N PACKAGE SN74LS08, SN74S08 . . . D, J OR N PACKAGE (TOP VIEW)

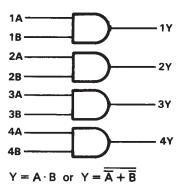
	_		
1A 🗆	1	U 14	□ vcc
18 □	2	13	] 4B
1Y 🗆	3	12	D 4A
2A 🗀	4	11	□ 4Y
28 🗆	5	10	] 3B
2Y 🗀	6	9	] 3A
GND [	7	8	] 3Y

SN54LS08, SN54S08 . . . FK PACKAGE (TOP VIEW)

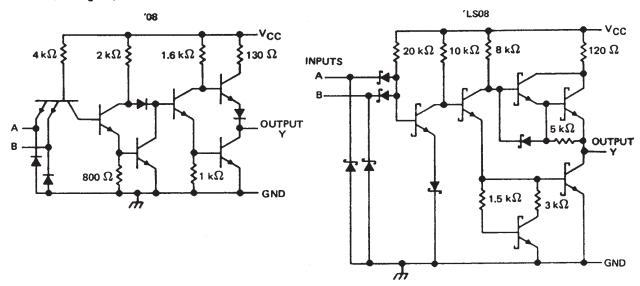


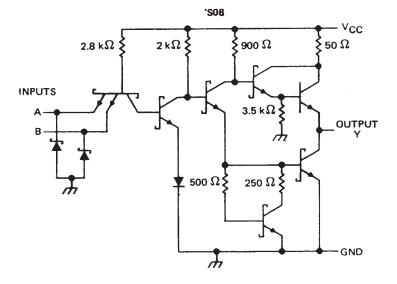
NC-No internal connection

### logic diagram (positive logic)



### schematics (each gate)





Resistor values are nominal.

## absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

Supply voltage, VCC (see Note 1)		7 V
Input voltage: '08, 'S08		5.5 V
Operating free-air temperature range:	SN54'	55°C to 125°C
	SN74'	0°C to 70°C
Storage temperature range		65°C to 150°C

NOTE 1: Voltage values are with respect to network ground terminal.



### recommended operating conditions

		SN5406	3	SN7408			UNIT
	MIN	NOM	MAX	MIN	NOM	MAX	UNIT
VCC Supply voltage	4.5	5	5.5	4.75	5	5.25	٧
VIH High-level input voltage	2			2			٧
V <sub>IL</sub> Low-level input voltage			0.8			8.0	٧
IOH High-level output current			- 0.8			- 0.8	mA
IOL Low-level output current			16			16	mA
TA Operating free-air temperature	- 55		125	0		70	°c

# electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

				SN540	3		SN740	В	UNIT
PARAMETER		TEST CONDITIONS T	MIN	TYP\$	MAX	MIN	TYP‡	MAX	UNIT
VIK	V <sub>CC</sub> = MIN,	I <sub>1</sub> = - 12 mA			<i>-</i> 1.5			- 1.5	V
Voн	V <sub>CC</sub> = MIN,	V <sub>1H</sub> = 2 V, I <sub>OH</sub> = -0.8 mA	2.4	3.4		2.4	3.4		.V
VOL	V <sub>CC</sub> = MIN,	V <sub>IL</sub> = 0.8 V, I <sub>OL</sub> = 16 mA		0.2	0.4		0.2	0.4	V
l <sub>į</sub>	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 5.5 V			1			1	mA
ин	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 2.4 V			40			40	μΑ
l <sub>I</sub> L	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0.4 V			- 1.6			- 1.6	mA
IOS§	V <sub>CC</sub> = MAX		- 20		- 55	- 18		- 55	mA
<sup>1</sup> ССН	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 4.5 V		11	21		11	21	mA
<sup>1</sup> CCL	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0 V		20	33		20	33	mA

<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

# switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDIT	TEST CONDITIONS				
tPLH						17.5	27	ns
tPHL	A or B	Y	$R_L = 400 \Omega$ ,	C <sub>L</sub> = 15 pF		12	19	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ . § Not more than one output should be shorted at a time.

### recommended operating conditions

			SN54LS	08		UNIT		
		MIN	NOM	MAX	MIN	NOM	MAX	UNII
vcc	Supply voltage	4.5	5	5.5	4.75	5	5.25	٧
VIН	High-level input voltage	2			2			٧
VIL	Low-level input voltage			0.7			0.8	٧
ЮН	High-level output current			- 0.4			- 0.4	mA
loL	Low-level output current			4			8	mA
TA	Operating free-air temperature	- 55		125	0		70	°c

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

					SN54LS	08		SN74LS	08	UNIT
PARAMETER		TEST CONDIT	TONS I	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	VCC = MIN,	I <sub>1</sub> = - 18 mA				- 1.5			- 1.5	٧
Voн	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	I <sub>OH</sub> = - 0.4 mA	2.5	3.4		2.7	3.4		٧
.,	V <sub>CC</sub> = MIN,	VIL = MAX,	I <sub>OL</sub> = 4 mA		0.25	0.4		0.25	0.4	V
VOL	VCC = MIN,	VIL = MAX,	IOL = 8 mA					0.35	0.5	v
11	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 7 V				0.1			0.1	mA
ΊΗ	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 2.7 V				20			20	μΑ
1 <sub>1</sub> L	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0.4 V				- 0.4		-	- 0.4	mA
los§	V <sub>CC</sub> = MAX			- 20		100	- 20		- 100	mA
<sup>1</sup> ссн	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 4.5 V			2.4	4.8		2.4	4.8	mA
ICCL	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0 V			4.4	8.8		4.4	8.8	mA

### switching characteristics, VCC = 5 V, TA = 25°C (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CON	MIN	TYP	MAX	UNIT	
tPLH	A or B	V	$R_1 = 2 k\Omega$ ,	C 15 oF		8	15	ns
<sup>t</sup> PHL	A OF B	'	11[ - 2 K14,	C <sub>L</sub> = 15 pF		10	20	ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



<sup>†</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions. ‡ All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.

### recommended operating conditions

			SN5	150	8		SN74S0	8	UNIT
		MIN	NO	M	MAX	MIN	NOM	MAX	UNII
Vcc	Supply voltage	4.5		5	5.5	4.75	5	5.25	٧
VIH	High-level input voltage	2				2			V
VIL	Low-level input voltage				8.0		_	0.8	V
ЮН	High-level output current				- 1			<b>–</b> 1	mA
loL	Low-level output current				20			20	mA
TA	Operating free-air temperature	- 55			125	0		70	°c

### electrical characteristics over recommended operating free-air temperature range (unless otherwise noted)

					SN54S0	8		UNIT		
PARAMETER		TEST CONDIT	TIONS T	MIN	TYP‡	MAX	MIN	TYP‡	MAX	UNIT
VIK	V <sub>CC</sub> = MIN,	I <sub>1</sub> = -18 mA				-1.2		_	-1.2	٧
V <sub>OH</sub>	V <sub>CC</sub> = MIN,	V <sub>IH</sub> = 2 V,	I <sub>OH</sub> = -1 mA	2.5	3.4		2.7	3.4		٧
V <sub>OL</sub>	V <sub>CC</sub> = MIN,	V <sub>1L</sub> = 0.8 V	I <sub>OL</sub> = 20 mA			0.5			0.5	٧
11	V <sub>CC</sub> = MAX,	V <sub>I</sub> ≈ 5.5 V				1			1	mA
Iн	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 2.7 V				50			50	μА
IL	V <sub>CC</sub> = MAX,	V <sub>1</sub> = 0.5 V				-2			-2	mA
1 <sub>OS</sub> §	V <sub>CC</sub> = MAX			-40		-100	-40		-100	mA
Іссн	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 4.5 V			18	32		18	32	mA
ICCL	V <sub>CC</sub> = MAX,	V <sub>I</sub> = 0 V			32	57		32	57	mA

<sup>1</sup> For conditions shown as MIN or MAX, use the appropriate value specified under recommended operating conditions.

## switching characteristics, $V_{CC} = 5 \text{ V}$ , $T_A = 25^{\circ}\text{C}$ (see note 2)

PARAMETER	FROM (INPUT)	TO (OUTPUT)	TEST CONDITIONS	MIN	TYP	MAX	UNIT
<sup>t</sup> PLH			$R_1 = 280 \Omega$ , $C_L = 15  pF$		4.5	7	ns
tPHL.	4 . 5	V	NC - 200 32, CC - 13 pr		5	7.5	ns
<sup>t</sup> PLH	A or B	Y	R <sub>1</sub> = 280 Ω, C <sub>1</sub> = 50 pF		6		ns
tPHL			R <sub>L</sub> = 280 Ω, C <sub>L</sub> = 50 ρF		7,5		ns

NOTE 2: Load circuits and voltage waveforms are shown in Section 1.



<sup>‡</sup> All typical values are at  $V_{CC} = 5 \text{ V}$ ,  $T_A = 25^{\circ}\text{C}$ . § Not more than one output should be shorted at a time, and the duration of the short-circuit should not exceed one second.





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### **PACKAGING INFORMATION**

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
JM38510/08003BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 08003BCA	Samples
JM38510/08003BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 08003BDA	Samples
JM38510/08003BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 08003BDA	Samples
JM38510/31004B2A	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004B2A	Samples
JM38510/31004B2A	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004B2A	Samples
JM38510/31004BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004BCA	Samples
JM38510/31004BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004BCA	Samples
JM38510/31004BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004BDA	Samples
JM38510/31004BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004BDA	Samples
JM38510/31004SCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004SCA	Samples
JM38510/31004SCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004SCA	Samples
JM38510/31004SDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004SDA	Samples
JM38510/31004SDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004SDA	Samples
M38510/08003BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 08003BCA	Samples
M38510/08003BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 08003BCA	Samples
M38510/08003BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 08003BDA	Samples



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Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan	Lead finish/ Ball material	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
M38510/08003BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 08003BDA	Samples
M38510/31004B2A	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004B2A	Samples
M38510/31004B2A	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004B2A	Samples
M38510/31004BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004BCA	Samples
M38510/31004BCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004BCA	Samples
M38510/31004BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004BDA	Samples
M38510/31004BDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004BDA	Samples
M38510/31004SCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004SCA	Samples
M38510/31004SCA	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004SCA	Samples
M38510/31004SDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004SDA	Samples
M38510/31004SDA	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	JM38510/ 31004SDA	Samples
SN54LS08J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN54LS08J	Samples
SN54LS08J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN54LS08J	Samples
SN54S08J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN54S08J	Samples
SN54S08J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SN54S08J	Samples
SN74LS08D	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS08	Samples
SN74LS08D	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS08	Sample
SN74LS08DBR	ACTIVE	SSOP	DB	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS08	Sample





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Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty			MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
SN74LS08DBR	ACTIVE	SSOP	DB	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS08	Samples
SN74LS08DR	ACTIVE	SOIC	D	14	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS08	Samples
SN74LS08DR	ACTIVE	SOIC	D	14	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS08	Samples
SN74LS08DRE4	ACTIVE	SOIC	D	14	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS08	Samples
SN74LS08DRE4	ACTIVE	SOIC	D	14	2500	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	LS08	Samples
SN74LS08N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74LS08N	Samples
SN74LS08N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74LS08N	Samples
SN74LS08NE4	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74LS08N	Samples
SN74LS08NE4	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type 0 to 70		SN74LS08N	Samples
SN74LS08NSR	ACTIVE	SO	NS	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS08	Samples
SN74LS08NSR	ACTIVE	so	NS	14	2000	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	74LS08	Samples
SN74S08D	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	S08	Samples
SN74S08D	ACTIVE	SOIC	D	14	50	RoHS & Green	NIPDAU	Level-1-260C-UNLIM	0 to 70	S08	Samples
SN74S08N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74S08N	Samples
SN74S08N	ACTIVE	PDIP	N	14	25	RoHS & Green	NIPDAU	N / A for Pkg Type	0 to 70	SN74S08N	Samples
SNJ54LS08FK	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54LS 08FK	Samples
SNJ54LS08FK	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type -55 to 125		SNJ54LS 08FK	Samples
SNJ54LS08J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type -55 to 125		SNJ54LS08J	Samples
SNJ54LS08J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type -55 to 125		SNJ54LS08J	Samples
SNJ54LS08W	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54LS08W	Samples



## PACKAGE OPTION ADDENDUM

9-Mar-2021

Orderable Device	Status (1)	Package Type	Package Drawing	Pins	Package Qty	Eco Plan (2)	Lead finish/ Ball material (6)	MSL Peak Temp	Op Temp (°C)	Device Marking (4/5)	Samples
SNJ54LS08W	ACTIVE	CFP	W	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54LS08W	Samples
SNJ54S08FK	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54S 08FK	Samples
SNJ54S08FK	ACTIVE	LCCC	FK	20	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54S 08FK	Samples
SNJ54S08J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54S08J	Samples
SNJ54S08J	ACTIVE	CDIP	J	14	1	Non-RoHS & Green	SNPB	N / A for Pkg Type	-55 to 125	SNJ54S08J	Samples

(1) The marketing status values are defined as follows:

ACTIVE: Product device recommended for new designs.

LIFEBUY: TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

NRND: Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

PREVIEW: Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

(2) RoHS: TI defines "RoHS" to mean semiconductor products that are compliant with the current EU RoHS requirements for all 10 RoHS substances, including the requirement that RoHS substance do not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, "RoHS" products are suitable for use in specified lead-free processes. TI may reference these types of products as "Pb-Free".

RoHS Exempt: TI defines "RoHS Exempt" to mean products that contain lead but are compliant with EU RoHS pursuant to a specific EU RoHS exemption.

**Green:** TI defines "Green" to mean the content of Chlorine (Cl) and Bromine (Br) based flame retardants meet JS709B low halogen requirements of <=1000ppm threshold. Antimony trioxide based flame retardants must also meet the <=1000ppm threshold requirement.

- (3) MSL, Peak Temp. The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.
- (4) There may be additional marking, which relates to the logo, the lot trace code information, or the environmental category on the device.
- (5) Multiple Device Markings will be inside parentheses. Only one Device Marking contained in parentheses and separated by a "~" will appear on a device. If a line is indented then it is a continuation of the previous line and the two combined represent the entire Device Marking for that device.
- (6) Lead finish/Ball material Orderable Devices may have multiple material finish options. Finish options are separated by a vertical ruled line. Lead finish/Ball material values may wrap to two lines if the finish value exceeds the maximum column width.



## **PACKAGE OPTION ADDENDUM**

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### OTHER QUALIFIED VERSIONS OF SN54LS08, SN54LS08-SP, SN54S08, SN74LS08. SN74S08:

Catalog: SN74LS08, SN54LS08, SN74S08

Military: SN54LS08, SN54S08

Space: SN54LS08-SP

NOTE: Qualified Version Definitions:

- Catalog TI's standard catalog product
- Military QML certified for Military and Defense Applications
- Space Radiation tolerant, ceramic packaging and qualified for use in Space-based application

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## TAPE AND REEL INFORMATION





A0	Dimension designed to accommodate the component width
B0	Dimension designed to accommodate the component length
K0	Dimension designed to accommodate the component thickness
W	Overall width of the carrier tape
P1	Pitch between successive cavity centers

### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



#### \*All dimensions are nominal

All difficulties are norminal												
Device	Package Type	Package Drawing		SPQ	Reel Diameter (mm)	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P1 (mm)	W (mm)	Pin1 Quadrant
SN74LS08DBR	SSOP	DB	14	2000	330.0	16.4	8.35	6.6	2.4	12.0	16.0	Q1
SN74LS08DR	SOIC	D	14	2500	330.0	16.4	6.5	9.0	2.1	8.0	16.0	Q1
SN74LS08NSR	SO	NS	14	2000	330.0	16.4	8.2	10.5	2.5	12.0	16.0	Q1

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#### \*All dimensions are nominal

Device	Package Type	Package Drawing	Pins	SPQ	Length (mm)	Width (mm)	Height (mm)
SN74LS08DBR	SSOP	DB	14	2000	853.0	449.0	35.0
SN74LS08DR	SOIC	D	14	2500	853.0	449.0	35.0
SN74LS08NSR	SO	NS	14	2000	367.0	367.0	38.0

# W (R-GDFP-F14)

## CERAMIC DUAL FLATPACK



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a ceramic lid using glass frit.
- D. Index point is provided on cap for terminal identification only.
- E. Falls within MIL STD 1835 GDFP1-F14



CERAMIC DUAL IN LINE PACKAGE



Images above are just a representation of the package family, actual package may vary. Refer to the product data sheet for package details.

4040083-5/G





CERAMIC DUAL IN LINE PACKAGE



- 1. All controlling linear dimensions are in inches. Dimensions in brackets are in millimeters. Any dimension in brackets or parenthesis are for reference only. Dimensioning and tolerancing per ASME Y14.5M.
- 2. This drawing is subject to change without notice.
- 3. This package is hermitically sealed with a ceramic lid using glass frit.
- His package is remitted by sealed with a ceramic its using glass mit.
   Index point is provided on cap for terminal identification only and on press ceramic glass frit seal only.
   Falls within MIL-STD-1835 and GDIP1-T14.



CERAMIC DUAL IN LINE PACKAGE



# D (R-PDSO-G14)

## PLASTIC SMALL OUTLINE



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Body length does not include mold flash, protrusions, or gate burrs. Mold flash, protrusions, or gate burrs shall not exceed 0.006 (0,15) each side.
- Body width does not include interlead flash. Interlead flash shall not exceed 0.017 (0,43) each side.
- E. Reference JEDEC MS-012 variation AB.



# D (R-PDSO-G14)

# PLASTIC SMALL OUTLINE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Publication IPC-7351 is recommended for alternate designs.
- D. Laser cutting apertures with trapezoidal walls and also rounding corners will offer better paste release. Customers should contact their board assembly site for stencil design recommendations. Refer to IPC-7525 for other stencil recommendations.
- E. Customers should contact their board fabrication site for solder mask tolerances between and around signal pads.



# N (R-PDIP-T\*\*)

## PLASTIC DUAL-IN-LINE PACKAGE

16 PINS SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- Falls within JEDEC MS-001, except 18 and 20 pin minimum body length (Dim A).
- The 20 pin end lead shoulder width is a vendor option, either half or full width.



## DB (R-PDSO-G\*\*)

## PLASTIC SMALL-OUTLINE

### **28 PINS SHOWN**



NOTES: A. All linear dimensions are in millimeters.

B. This drawing is subject to change without notice.

C. Body dimensions do not include mold flash or protrusion not to exceed 0,15.

D. Falls within JEDEC MO-150

# FK (S-CQCC-N\*\*)

## LEADLESS CERAMIC CHIP CARRIER

28 TERMINAL SHOWN



- A. All linear dimensions are in inches (millimeters).
- B. This drawing is subject to change without notice.
- C. This package can be hermetically sealed with a metal lid.
- D. Falls within JEDEC MS-004



## **MECHANICAL DATA**

# NS (R-PDSO-G\*\*)

# 14-PINS SHOWN

### PLASTIC SMALL-OUTLINE PACKAGE



- A. All linear dimensions are in millimeters.
- B. This drawing is subject to change without notice.
- C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15.



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